

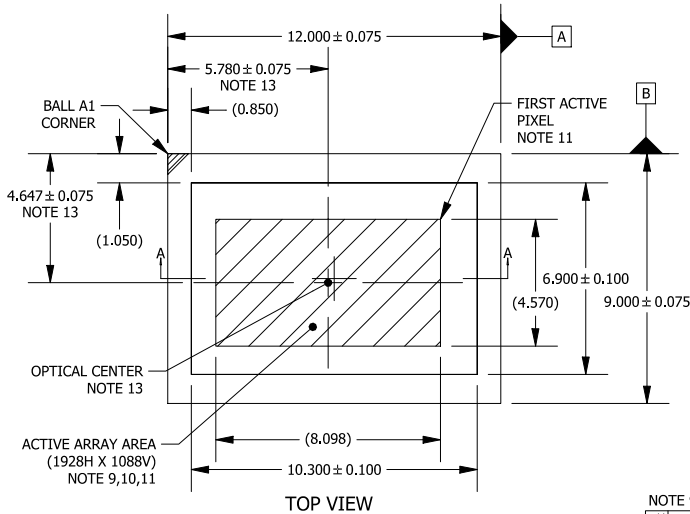
# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®



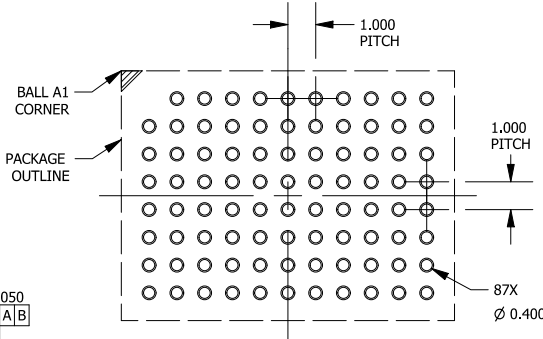
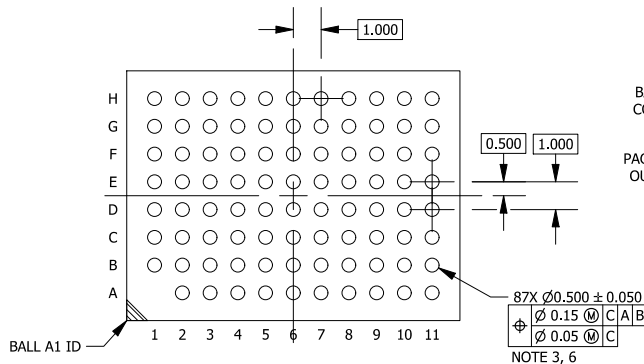
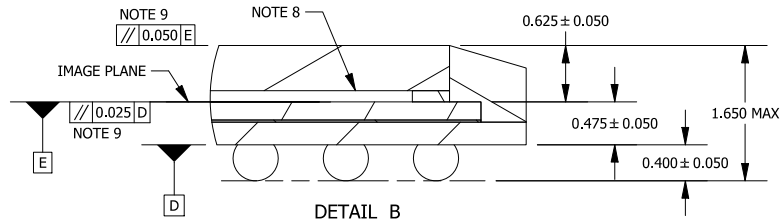
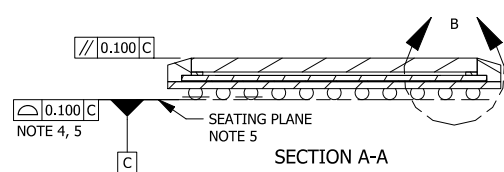
## IBGA87 12x9 CASE 503BR ISSUE B

DATE 21 FEB 2020

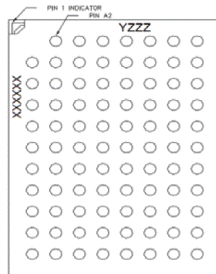


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS [mm].
3. SOLDER BALL DIAMETER IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER PARALLEL TO DATUM C.
4. COPLANARITY APPLIES TO THE SPHERICAL CROWNS OF THE SOLDER BALLS.
5. DATUM C, THE SEATING PLANE IS DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.
6. SOLDER BALL MATERIAL: SAC305 (96.5% Sn, 3% Ag, 0.5% Cu).
7. GLASS: 0.500 THICKNESS; REFRACTIVE INDEX = 1.52; AR COATING R < 1% 420-850nm (EACH SIDE).
8. AIR GAP BETWEEN GLASS AND PIXEL ARRAY: 0.125 THICKNESS.
9. PARALLELISM APPLIES ONLY TO THE ACTIVE ARRAY.
10. MAXIMUM ROTATION OF ACTIVE ARRAY RELATIVE TO DATUMS A AND B IS ± 0.7°.
11. REFER TO THE DEVICE DATA SHEET FOR TOTAL PIXEL ARRAY DEFINITIONS.
12. PACKAGE CENTER (X, Y) = (0.000, 0.000).
13. OPTICAL CENTER RELATIVE TO PACKAGE CENTER (X, Y) = (-0.220, -0.147).



### GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code  
Y = Year  
ZZZ = Lot Traceability

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

### RECOMMENDED MOUNTING FOOTPRINT\*

\*FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

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<b>DESCRIPTION:</b>	<b>IBGA87 12X9</b>	<b>PAGE 1 OF 1</b>

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